

ABSTRACT OF THE DISCLOSURE

An oscillator circuit having an oscillation period moderately varying such that it is short at high temperature but long at low temperature and wherein a maximum value of oscillation period at low temperature can be set. By coupling a resistance parallel circuit having a resistance element having a resistance value decreasing with increasing temperature and a resistance element having a resistance value nondependent upon temperature at between the main electrodes of PMOST and NMOST, the output signal of an inverter is caused to vary with temperature. A ring oscillator circuit outputs an oscillation period short at high temperature but long at low temperature. Meanwhile, because oscillation period is greatly affected by a resistance value of the resistant element not dependent upon temperature at low temperature, a maximum value of oscillation period can be set.